

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2295	716/4.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:02
L2	1700	716/5.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:19
L3	47421	(board or pcb or pwb) and (layout or floorplan\$4 or (floor adj plan\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 12:01
L4	543	(bypass adj capacitor) and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:08
L5	25	(impedance with (power near4 (pin or terminal))) and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 11:53
L6	11	(power with via) and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:14
L7	11	(power near4 via) and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:14
L8	1	(impedance with compar\$3) and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:19
L9	1	(impedance same compar\$3) and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:32
L10	7	compar\$3 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:19
L11	1420	716/6.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:34

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L12	28	(impedance near4 (calculat\$3 or computing or computed or defin\$3)) and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 12:18
L13	4	(impedance with compar\$3) and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 11:09
L14	214	716/15.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:35
L15	10	(impedance same compar\$3) and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:46
L16	2265	(board or pcb or pwb) and (bypass adj capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 12:48
L17	66	(impedance near4 compar\$3) and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 12:53
L18	12	(impedance same (power near4 (pin or terminal))) and 17	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 12:49
L19	12	((power near4 source) with (power near4 (pin or terminal))) and 17	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 11:58
L20	5	((bypass adj capacitor) with (power near4 (pin or terminal))) and 19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 11:56
L21	75	((power near4 source) with (power near4 (pin or terminal))) same ((bypass adj capacitor) with (power near4 (pin or terminal)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 11:59
L22	13	((power near4 source) with (power near4 (pin or terminal))) same ((bypass adj capacitor) with (power near4 (pin or terminal))) same impedance	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 12:50
L23	4	22 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 12:01

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L24	2	(impedance near4 (calculat\$3 or computing or computed or defin\$3)) and 23	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 12:52
L25	40	(board or pcb or pwb).clm. and (bypass adj capacitor).clm.	US-PGPUB	OR	ON	2006/06/16 12:48
L27	1	((power near4 source) with (power near4 (pin or terminal))) same ((bypass adj capacitor) with (power near4 (pin or terminal))).clm. and 25	US-PGPUB	OR	ON	2006/06/16 12:51
L28	1	((power near4 source) with (power near4 (pin or terminal))).clm. and 25	US-PGPUB	OR	ON	2006/06/16 12:55
L29	8	((bypass adj capacitor) with (power near4 (pin or terminal))).clm. and 25	US-PGPUB	OR	ON	2006/06/16 12:52
L30	0	(impedance near4 (calculat\$3 or computing or computed or defin\$3)).clm. and 29	US-PGPUB	OR	ON	2006/06/16 12:52
L32	1	(impedance same compar\$3).clm. and 25	US-PGPUB	OR	ON	2006/06/16 12:54
L33	1	impedance.clm. and compar\$3.clm. and 25	US-PGPUB	OR	ON	2006/06/16 12:55
L34	1	((power near4 source) same (power near4 (pin or terminal))).clm. and 25	US-PGPUB	OR	ON	2006/06/16 12:55
L35	1	((power near4 source) and (power near4 (pin or terminal))).clm. and 25	US-PGPUB	OR	ON	2006/06/16 12:55